

RECEIVED  
CENTRAL FAX CENTER

DEC 17 2004

CUSTOMER NO. 32,986  
IP STRATEGY GROUP, P.C.  
Intellectual Property Law Office  
P.O. Box 700640  
San Jose, CA 95170-0640

PATENT APPLICATION  
ATTORNEY DOCKET NO. ANTAP004/IC0105

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

<p>In re application of: ACHLER Serial No.: 10/026,679 Filed: December 17, 2001</p> <p>Title: INTERFACE CIRCUITS FOR MODULARIZED DATA OPTIMIZATION ENGINES AND METHODS THEREFOR</p>	<p>Group Art Unit: 2666 Examiner: RAO, SEEMA SRINIVAS Docket: ANTAP004/IC-0105 Confirmation No.: 7042</p> <p>CERTIFICATE OF FACSIMILE TRANSMISSION I hereby certify that this correspondence is being faxed to the USPTO at 703-872-9306 on December 17, 2004. <u>2 PAGES TOTAL</u> (inclusive).</p> <p>Signed: /Hanh H. Bui/ Hanh H. Bui</p>
---	---

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §§ 1.56 AND 1.97(c)**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to the patentability of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the patentability of this application does not exist, or that these references indeed constitute prior art.

If the Commissioner determines that additional fees are required to process this Information Disclosure Statement, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-2284 (Order No. ANTAP004).

Respectfully submitted,

/Joseph A. Nguyen/  
Joseph A. Nguyen  
Reg. No. 37,899

RECEIVED  
CENTRAL FAX CENTER

DEC 17 2004

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: ACHLER Serial No.: 10/026,679 Filed: December 17, 2001  Title: INTERFACE CIRCUITS FOR MODULARIZED DATA OPTIMIZATION ENGINES AND METHODS THEREFOR	Group Art Unit: 2666 Examiner: RAO, SEEMA SRINIVAS Docket: ANTAP004/IC-0105 Confirmation No.: 7042
--	---

INFORMATION DISCLOSURE STATEMENTUS PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date	Name of Patentee or Applicant	Reference to Related Case

FOREIGN PATENT DOCUMENTS

Examiner Initials	Cite No.	Document Number	Publication Date	Applicant	Reference to Related Case	T

OTHER DOCUMENTS

Examiner Initials	Cite No.	Description	T
		US Patent Application No.: 10/026,370 Filed: December 17, 2001 Title: "Architectures for a modularized data optimization engine and methods therefore" Docket: ANTAP001/IC-0102	
		US Patent Application No.: 10/026,680 Filed: December 17, 2001 Title: "Integrated circuits for a high speed adaptive compression and methods therefore" Docket: ANTAP002/IC-0103	
		US Patent Application No.: 10/026,368 Filed: December 17, 2001 Title: "Interface receive circuits for modularized data optimization engines and methods therefore" Docket: ANTAP005/IC-0106	

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--